

Application No. 10/026,125

Atty Docket No. MXIC 1517-1

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In the Specification:

Please amend paragraph 14 on page 4 as follows:

[0014] **FIG. 3** adds an additional bake ~~[[311]]~~ **312**, following the pre-erase **211**. This bake should be under conditions sufficient to diffuse charges added to the floating gate **402** or the ONO layer **412** by the erase. Two different temperature ranges appear to be useful: 80-150 degrees Celsius and 150-250 degrees Celsius. The time required for sufficient effect can readily be determined, without an excessive degree of experimentation.